



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/708,489	03/07/2004	YUAN-HSUN CHANG	12447-US-PA	2488
31561 7590 06/15/2006				
JIANQ CHYUN INTELLECTUAL PROPERTY OFFICE				
7 FLOOR-1, NO. 100				
ROOSEVELT ROAD, SECTION 2				
TAIPEI, 100				
TAIWAN				
			EXAMINER	
			HAN, YOUNGHUIE JESSICA	
			ART UNIT	PAPER NUMBER
			2838	
DATE MAILED: 06/15/2006				

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/708,489	CHANG ET AL.	
	Examiner	Art Unit	
	Y. J. Han	2838	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) ☐ Responsive to communication(s) filed on ____.

2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.

3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) ☒ Claim(s) 1-8 is/are pending in the application.

4a) Of the above claim(s) ____ is/are withdrawn from consideration.

5) ☐ Claim(s) ____ is/are allowed.

6) ☒ Claim(s) 1-8 is/are rejected.

7) ☐ Claim(s) ____ is/are objected to.

8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

9) ☐ The specification is objected to by the Examiner.

10) ☒ The drawing(s) filed on 07 March 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) ☐ All b) ☐ Some * c) ☐ None of:

1. ☐ Certified copies of the priority documents have been received.

2. ☐ Certified copies of the priority documents have been received in Application No. ____.

3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) <input type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. ____.
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
Paper No(s)/Mail Date ____.	6) <input type="checkbox"/> Other: ____.

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1 and 5-6 are rejected under 35 U.S.C. 102(b) as being anticipated by Ooishi (5,689,460).

Ooishi discloses a voltage regulator apparatus, comprising: a voltage regulator having a output terminal to provide an output voltage regulated according to a reference voltage (V_{ref}); a first transistor (N3) having a first terminal coupled to a positive terminal of a voltage source (node 1), a second terminal coupled to a first bias (V_{cnn}), and a third terminal (node 6) coupled to the output terminal (5) of the voltage regulator; and a second transistor (P3) having a first terminal (node 6) coupled to the third terminal of the first transistor, a second terminal coupled to a second bias (V_{cpp}), and a third terminal coupled to a negative terminal (V_{ss}) of the voltage source. See figure 11. Note that N3 is an NMOS transistor and P3 is a PMOS transistor.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 2-4 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ooishi (5,689,460) in view of the acknowledged prior art.

Ooishi discloses the invention substantially as claimed but does not disclose an error amplifier connected to the structure recited in claim 1. The acknowledged prior art clearly teaches that the use of error amplifier is well known in the art. Figure 2 discloses explicitly an error amplifier having a positive input terminal, a negative input terminal, and an output terminal, wherein the negative input terminal is for receiving the reference voltage; a third transistor having a first terminal coupled to the positive terminal of the voltage source, a second terminal coupled to the output terminal of the error amplifier, and a third terminal outputting the regulated output voltage; and a load circuit used to divide the regulated output voltage, and provide a feedback voltage to the positive terminal of the error amplifier; wherein the load circuit comprises: a first resistor having a first terminal to receive the regulated output voltage, and a second terminal to output the feedback voltage to the positive terminal of the error amplifier; and a second resistor having a first terminal coupled to the second terminal of the first resistor, and a second terminal coupled to the negative terminal of the voltage source. Therefore, it would have been obvious to one having ordinary skill in the art to employ the error amplifier of the acknowledged prior art in Ooishi to obtain the claimed invention for the purpose of providing stable output voltage.

Response to Argument

The applicant's argument directed to the differences between the instant invention and the applied reference has been fully considered but is not deemed to be persuasive as applied to the claims.

Applicant contends that Ooishi does not disclose “a third terminal coupled to the output terminal of the voltage regulator” as set forth in claim 1. Specifically, applicant points out that the node 6 is coupled to the gate of the transistor 2 having a drain terminal coupled to the item 5. In other words, the node 6 is not directly coupled to the item 5. However, the claims do not call for the third terminal (node 6) to be directly coupled to the output terminal (item) of the voltage regulator. The claims merely call for the third terminal to be coupled to the output terminal, even though they are coupled through the transistor 2. Hence, claims fail to set forth any structure which is different from the structure disclosed in Ooishi.

As for applicant’s argument that Ooishi “does not provide a feedback voltage feedback to the comparator,” one cannot show non-obviousness by attacking the primary reference only where, as here, the rejection to claim 2 is based on combination of references. Besides, the voltage feedback feature is well known feature disclosed in the acknowledged prior art. Fig. 2 of the acknowledged prior art discloses that such feature is clearly old and well known in the art.

Conclusion

5. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

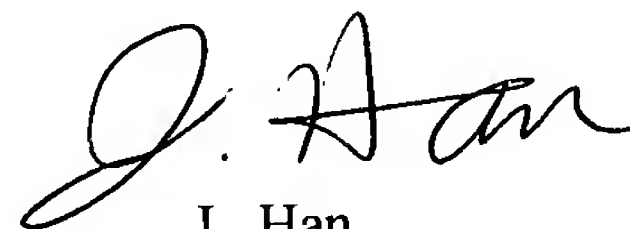
A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event,

however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Y. J. Han whose telephone number is 571-272-2078. The examiner can normally be reached on Mon-Fri 6:30am-3:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Karl Easthom can be reached on 571-272-1989. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



J. Han
Primary Examiner
Art Unit 2838